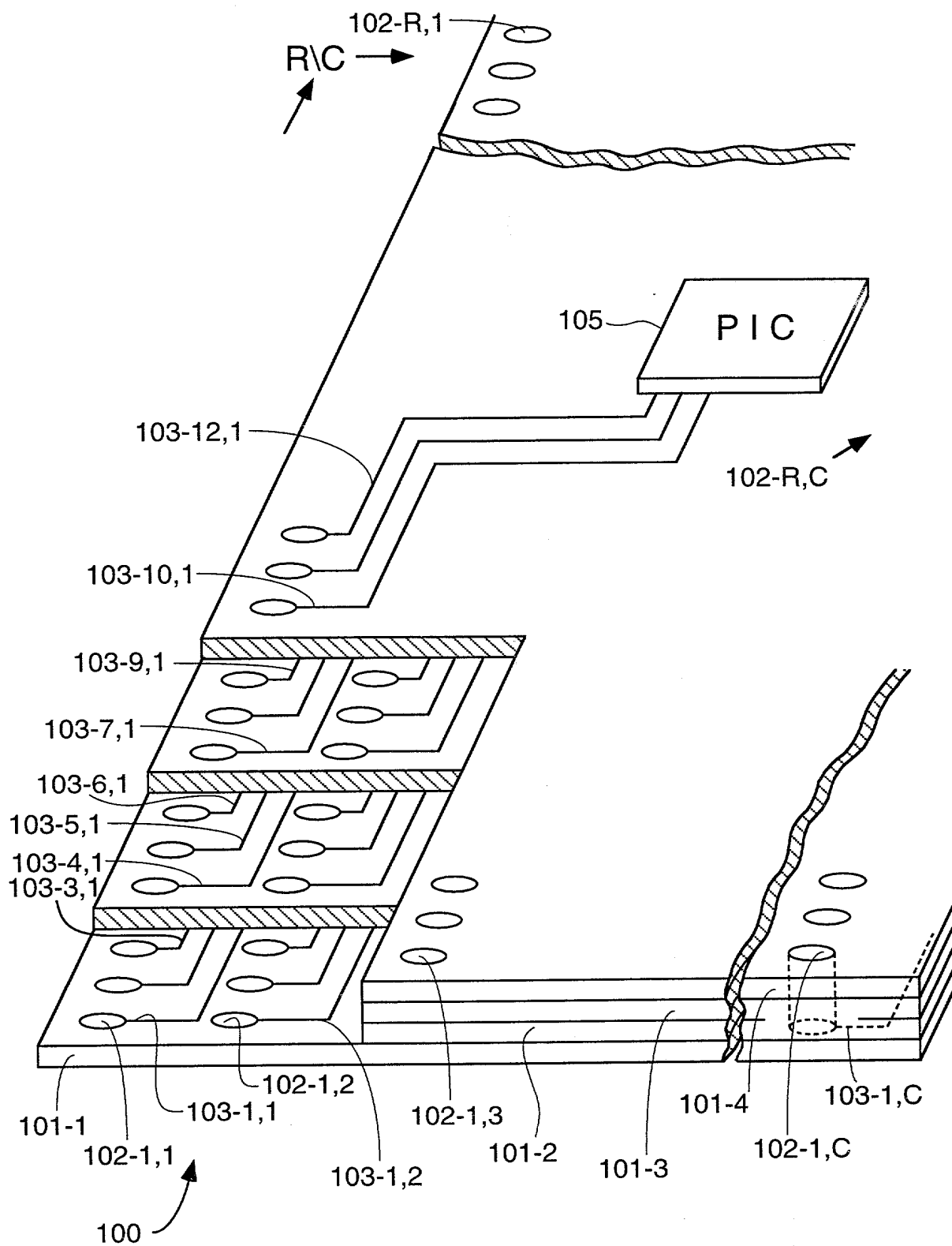


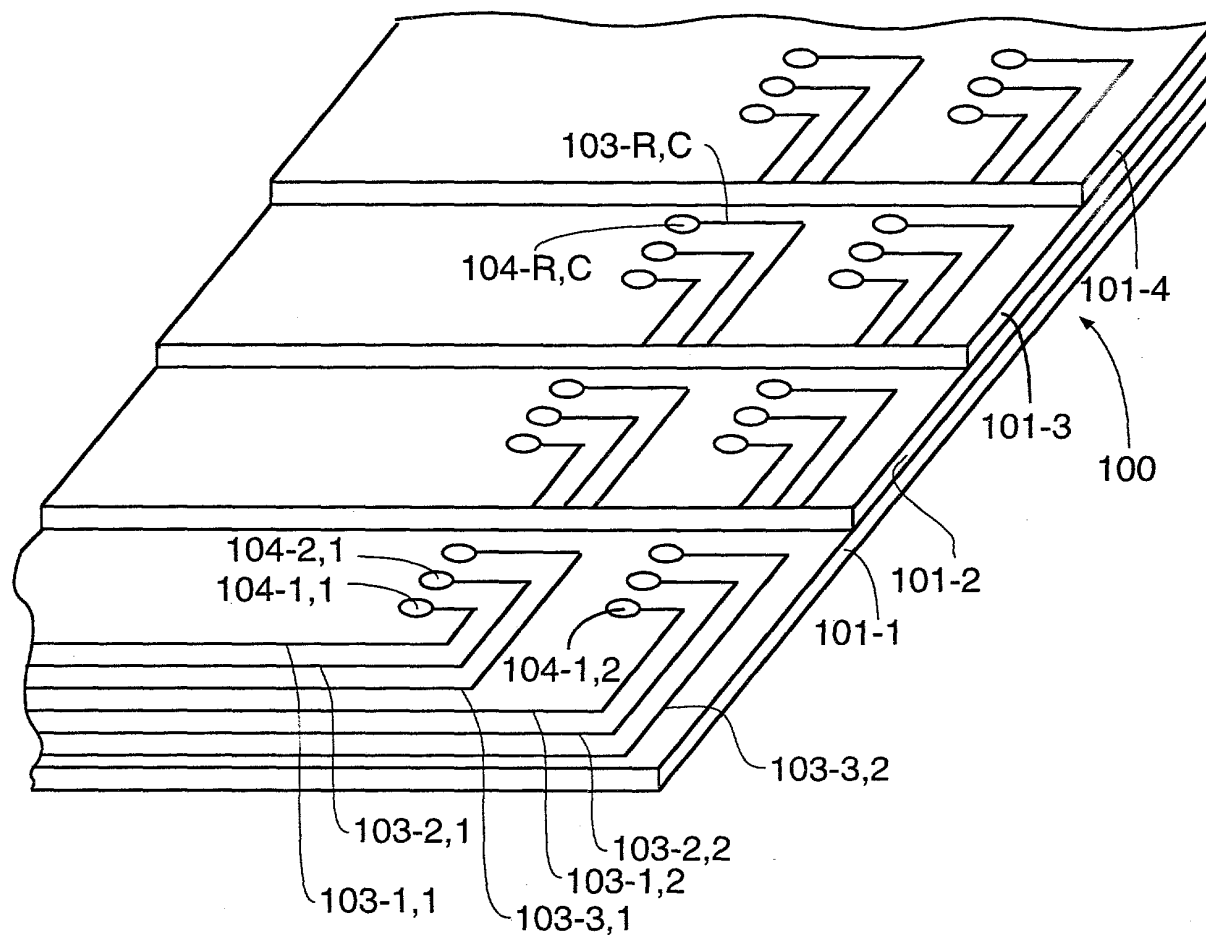
FIGURE 1a

1/21



2/21

FIGURE 1b



3/21

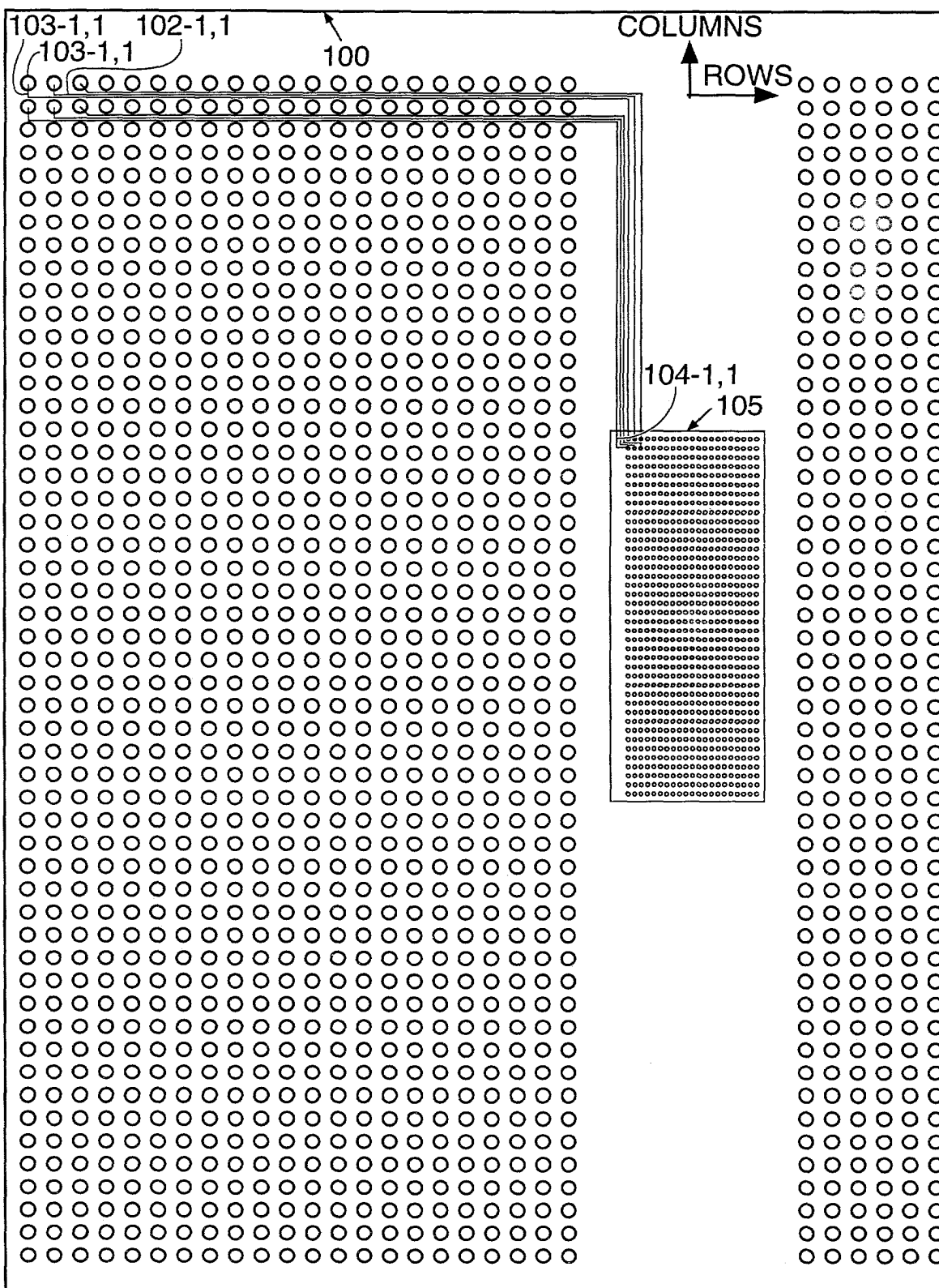


FIGURE 2a

4/21

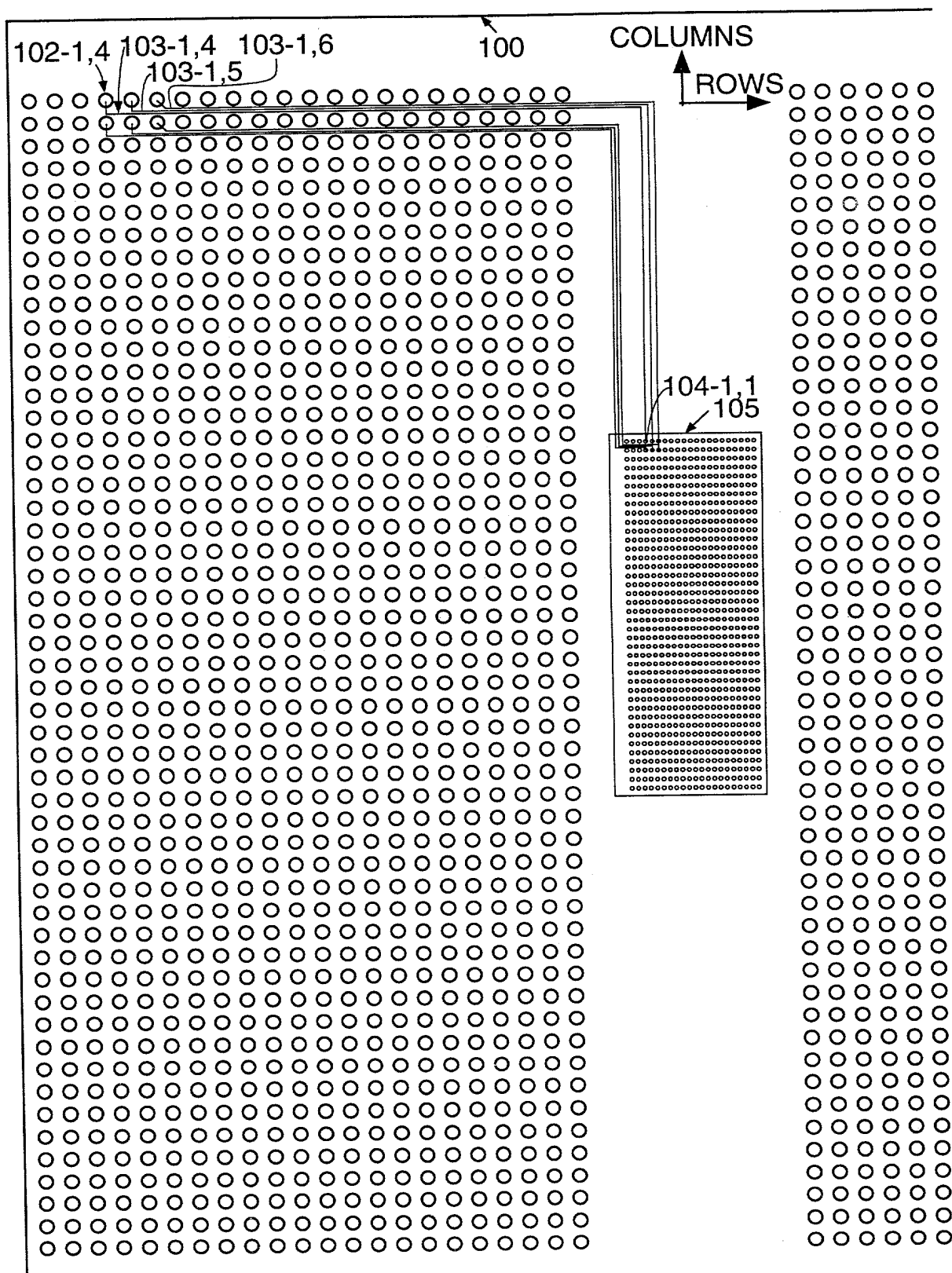


FIGURE 2b

5/21

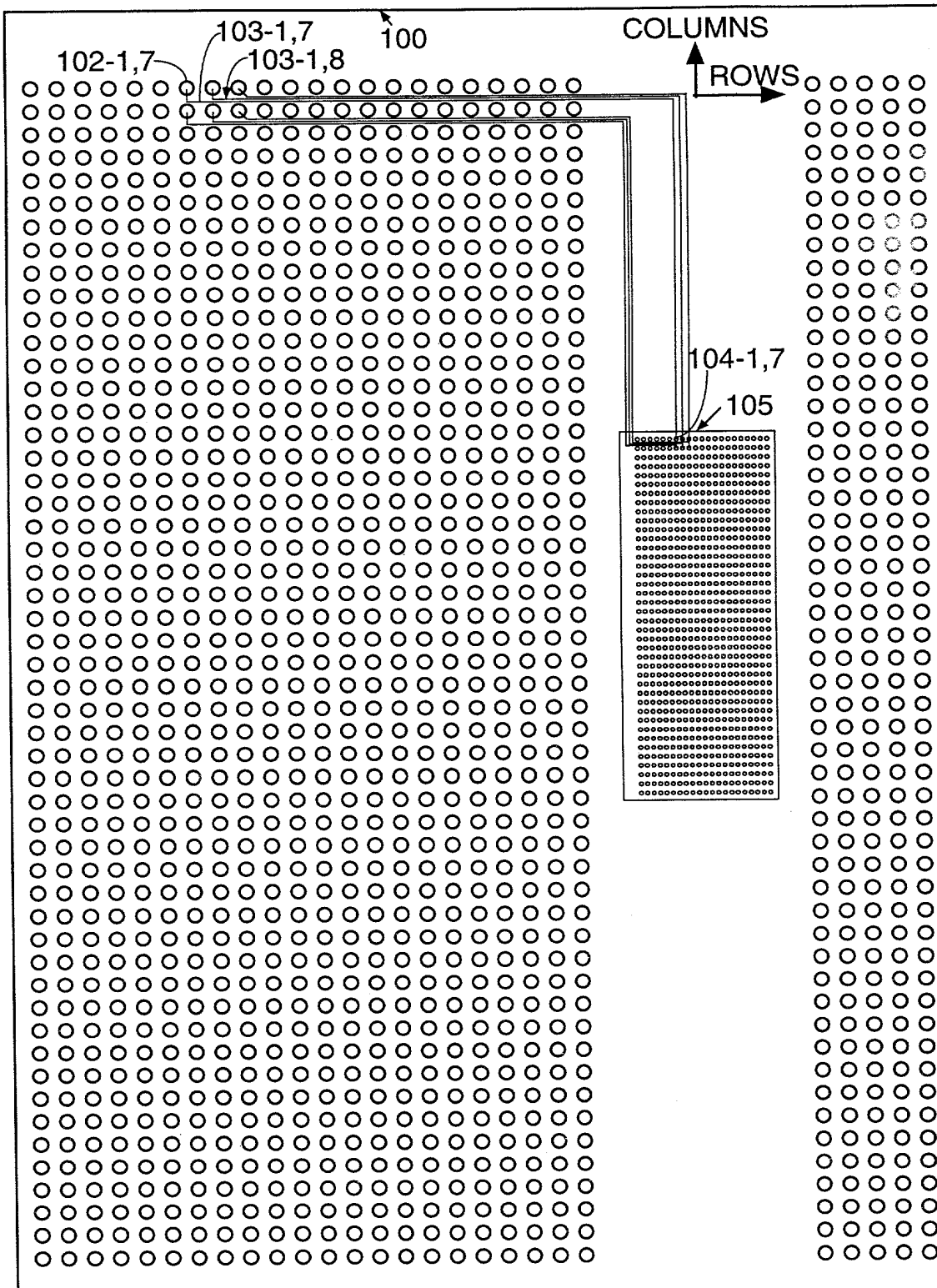


FIGURE 2c

6/21

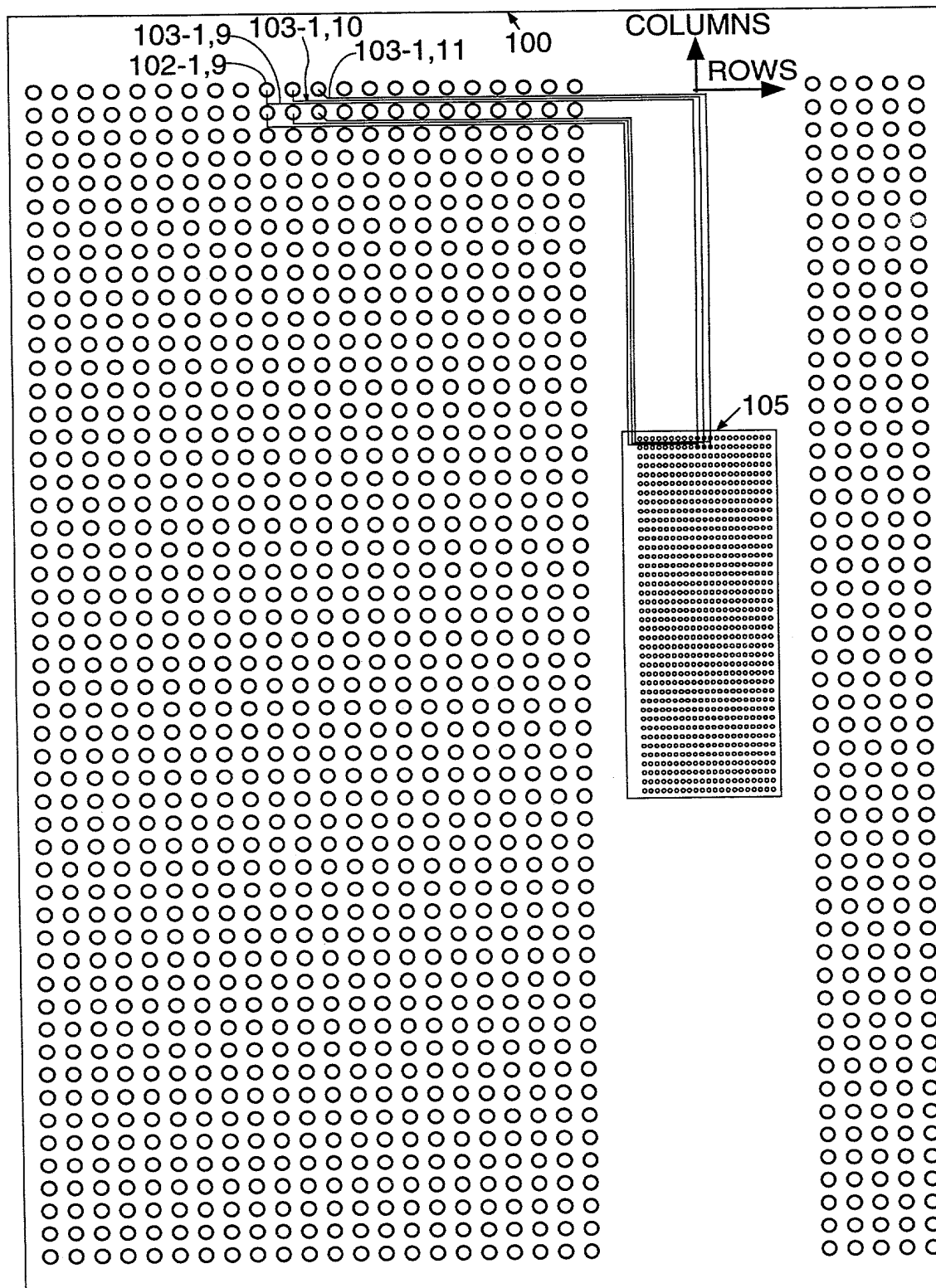
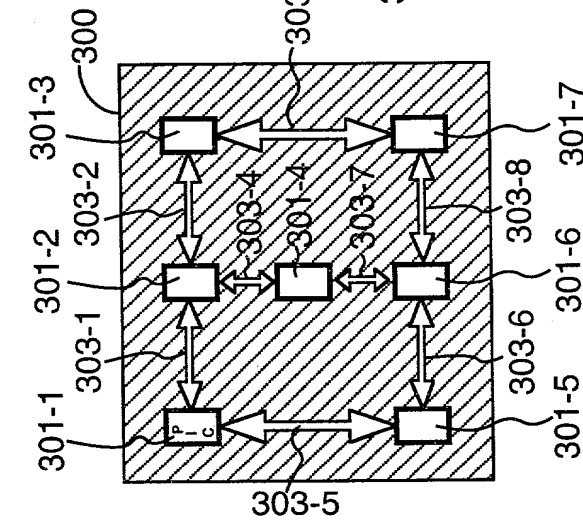


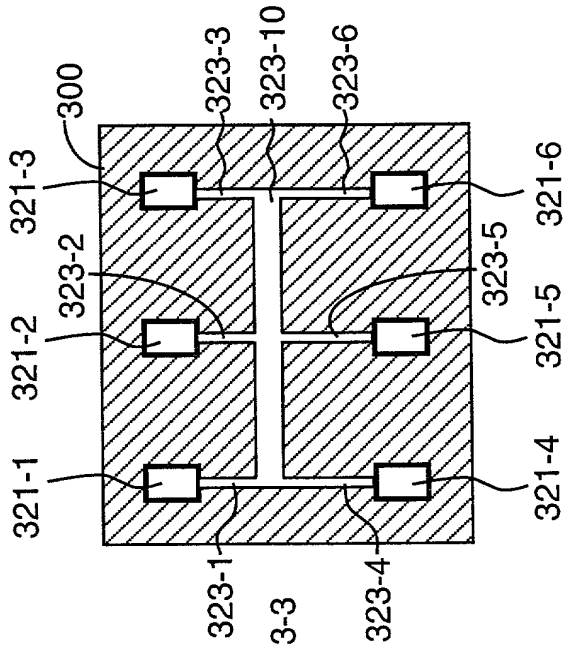
FIGURE 2d

GLOBAL INTERCONNECT ARCHITECTURE



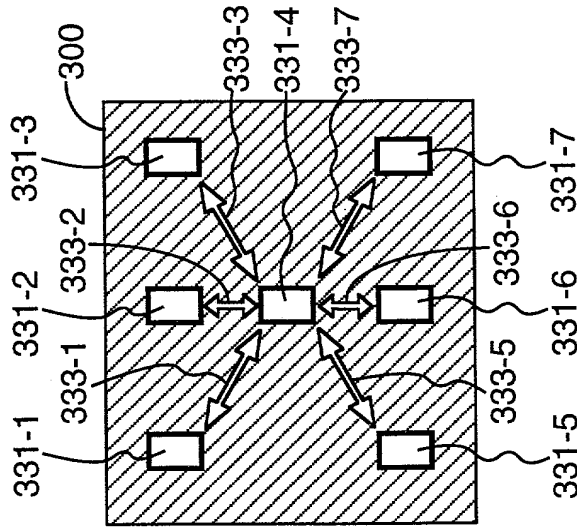
NEXT NEIGHBOR

FIGURE 3a



BUS

FIGURE 3c



RANDOM FREEWAYS

FIGURE 3d

8/21

HIERARCHY OF GLOBAL INTERCONNECTS

NEXT NEIGHBOR

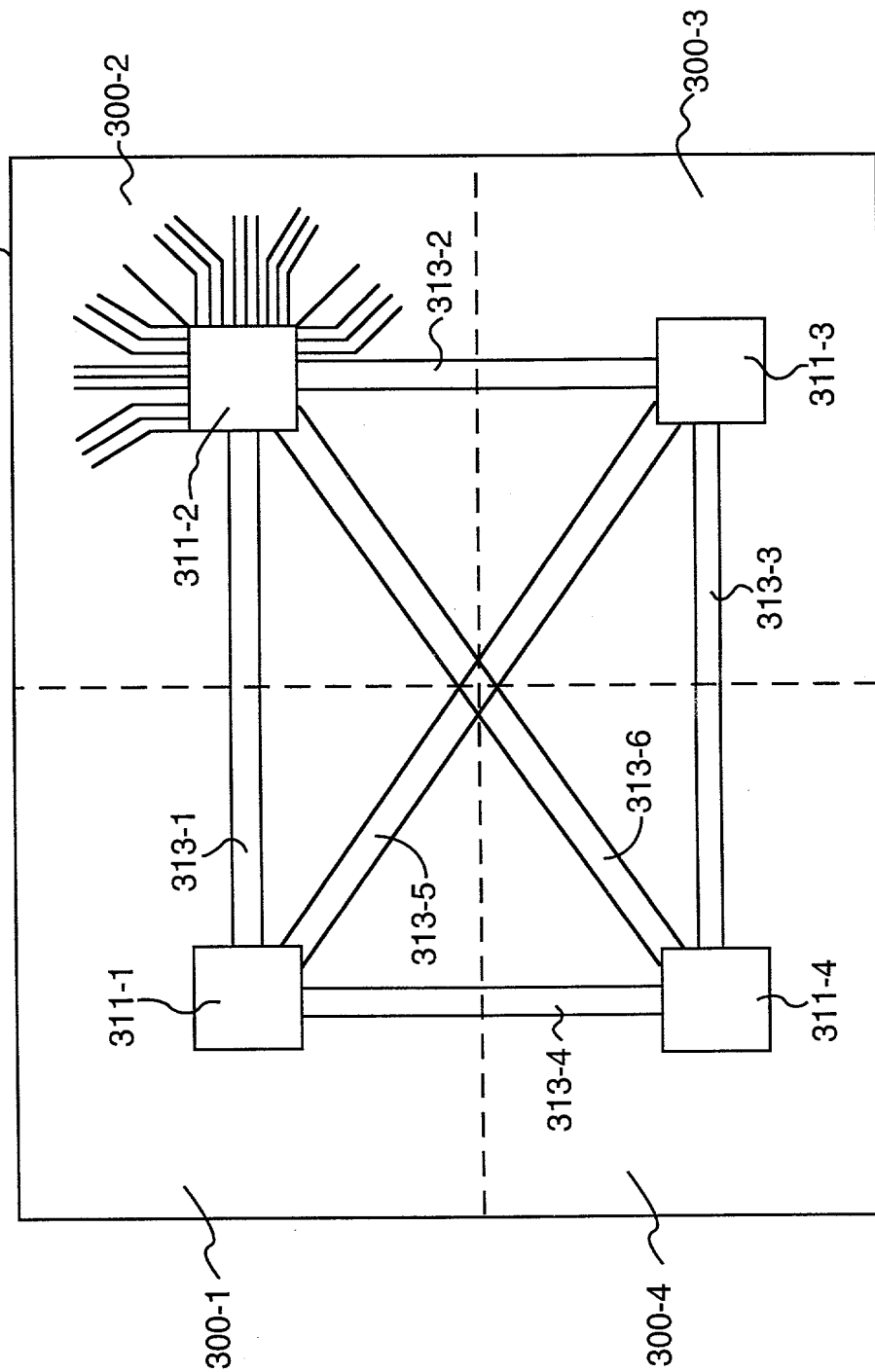
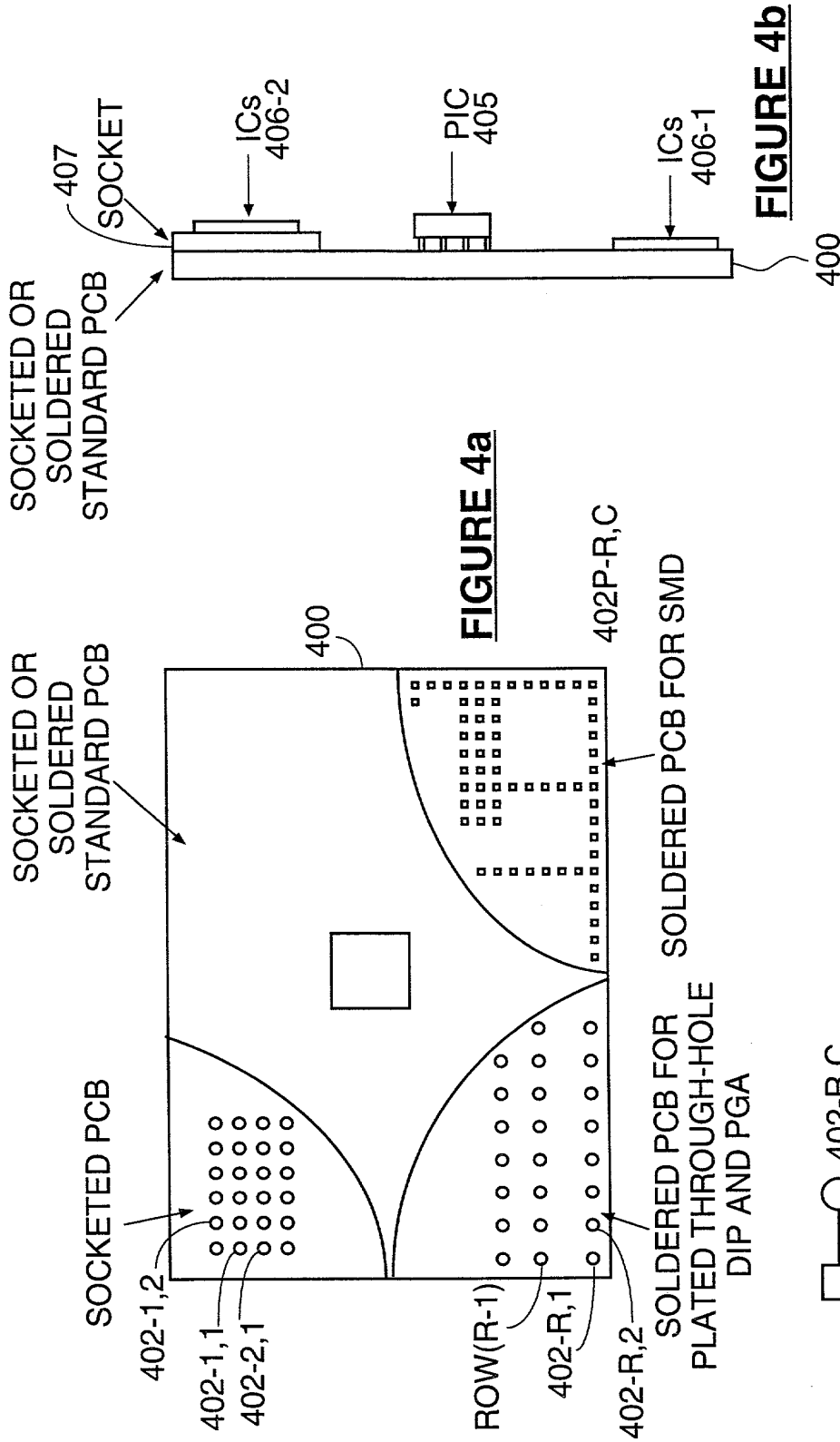


FIGURE 3b

9/21

FIELD PROGRAMMABLE PCB



10/21

CONTROL PORT AND TEST PORT/ DIAGNOSTIC TOOL

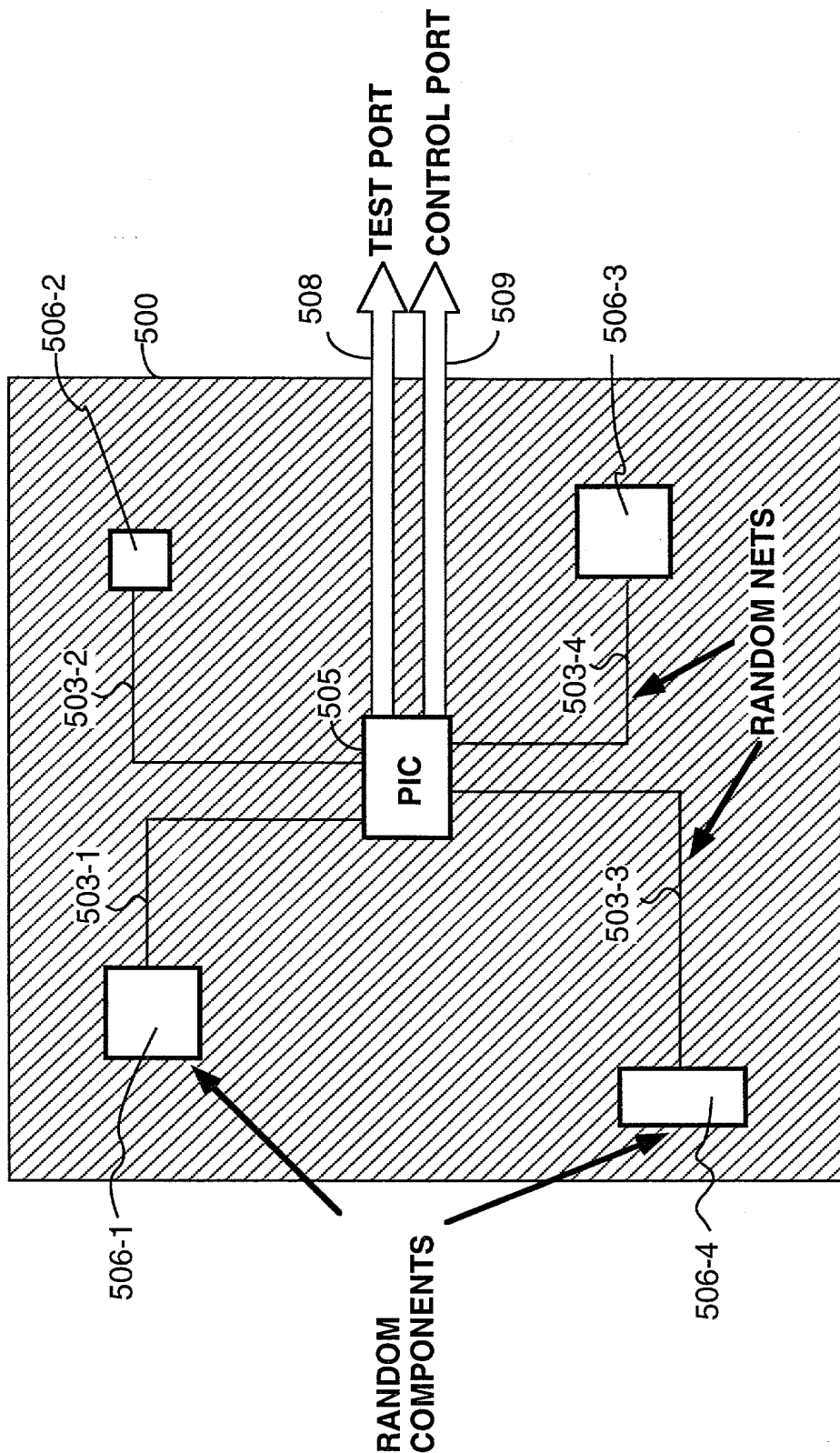


FIGURE 5

11/21

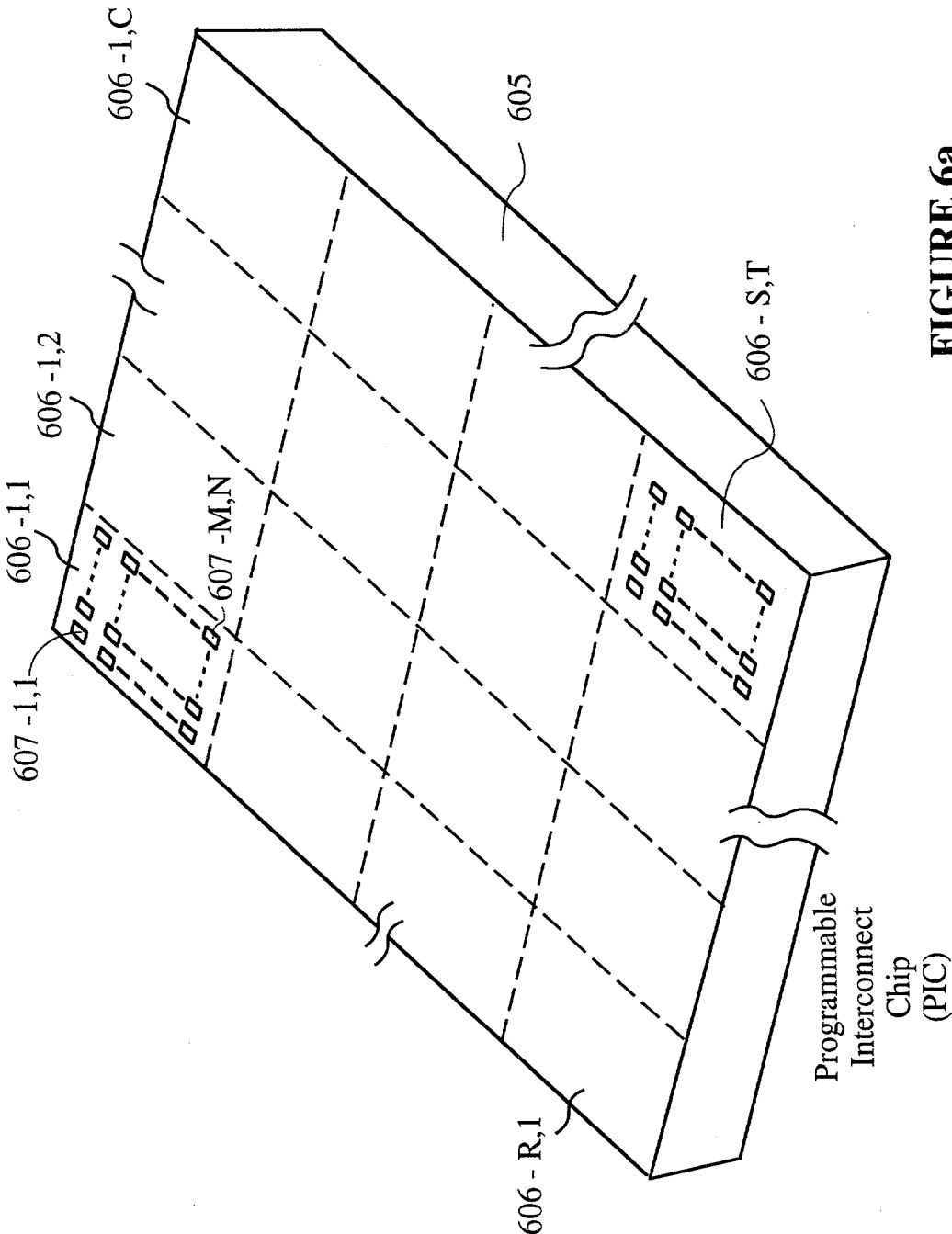


FIGURE 6a

12/21

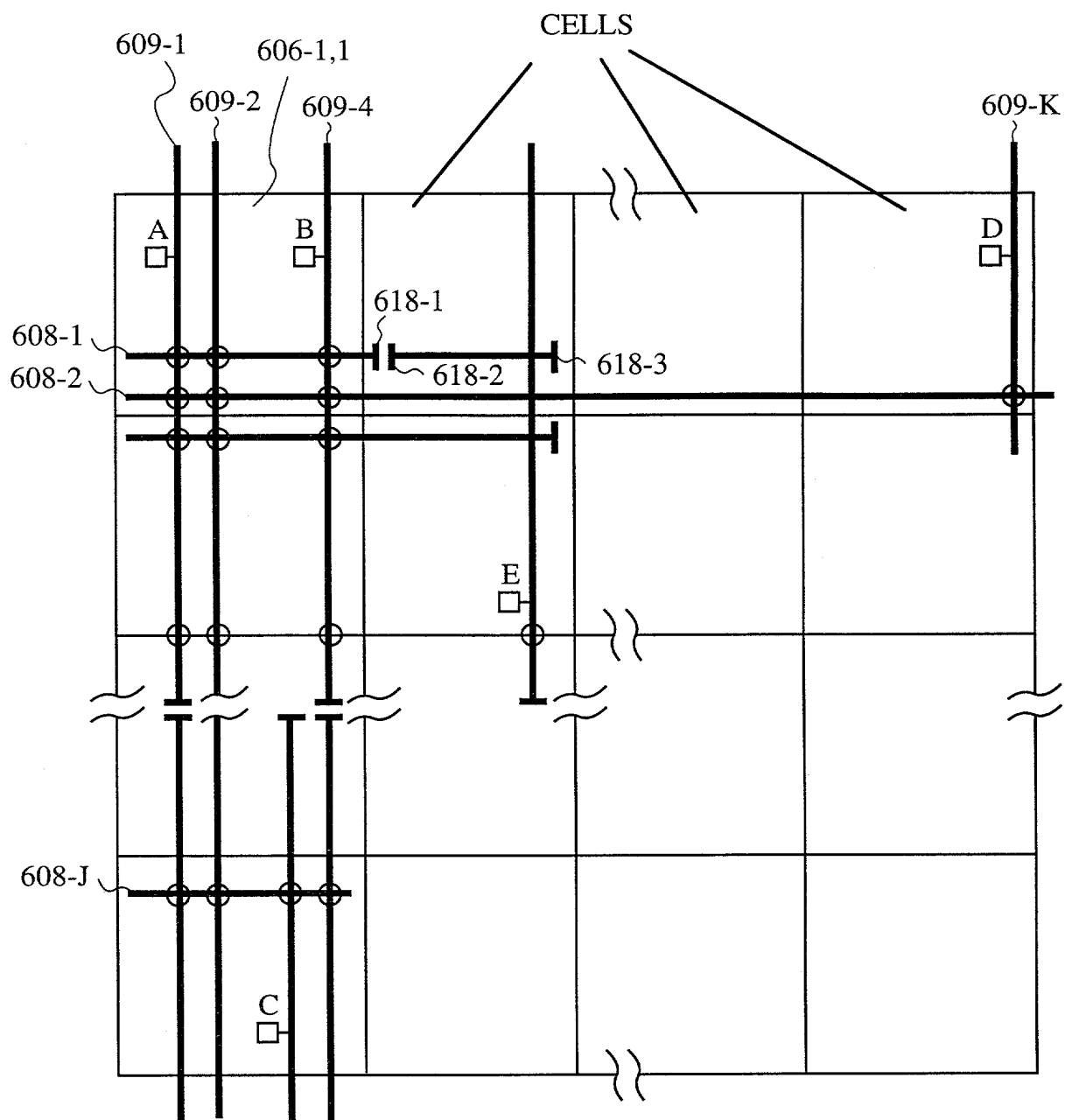


FIGURE 6b

13/21

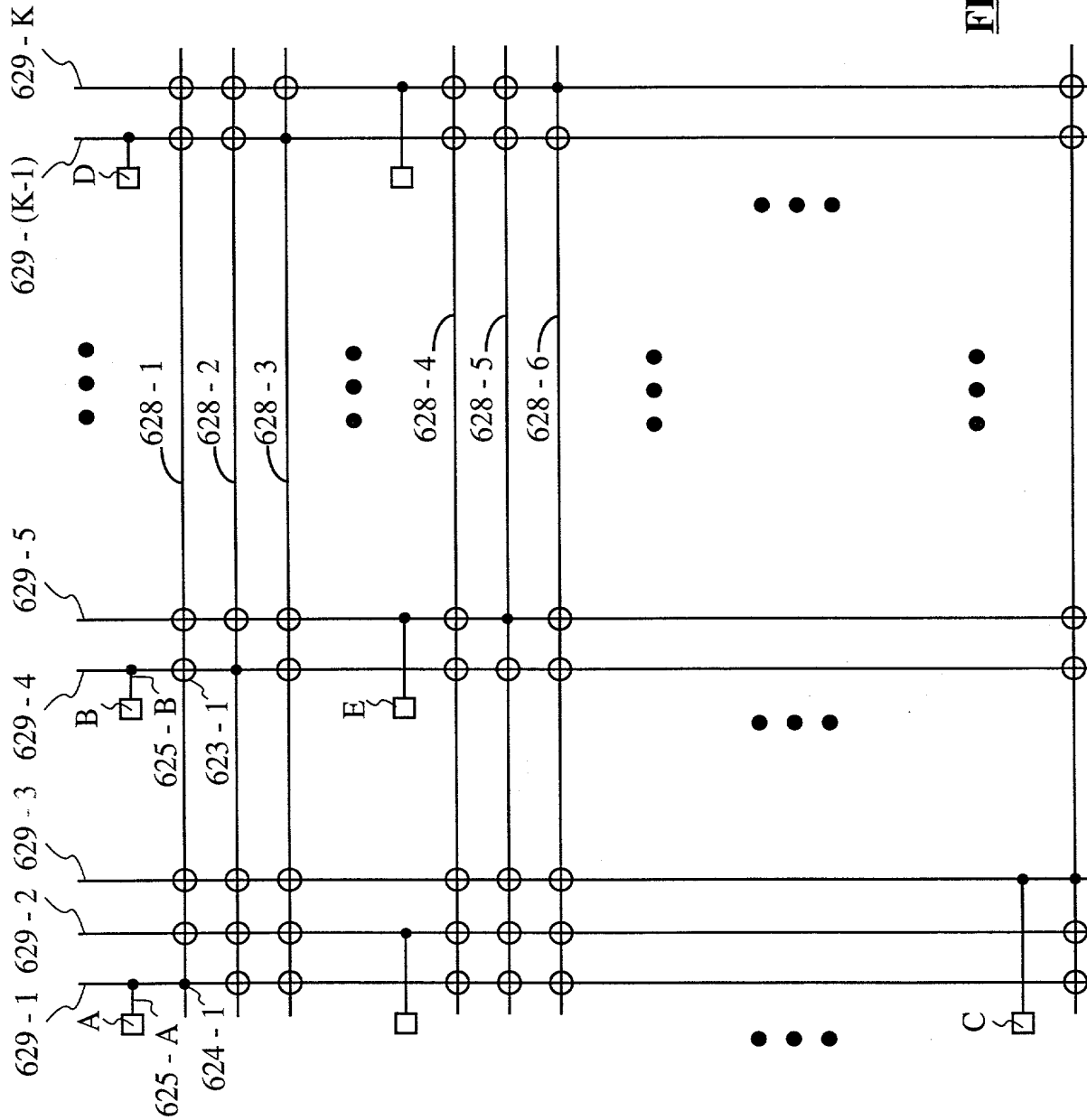
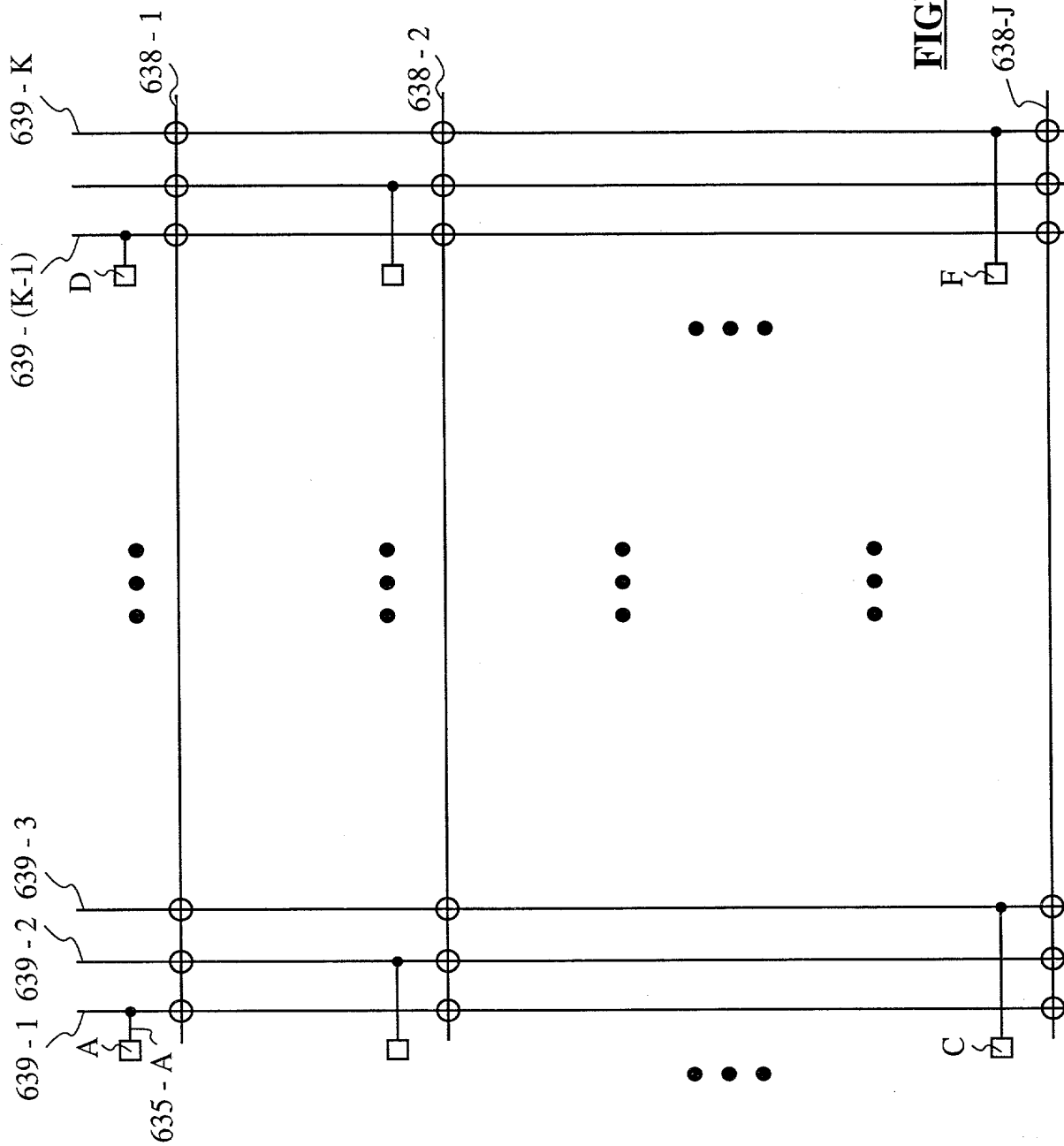
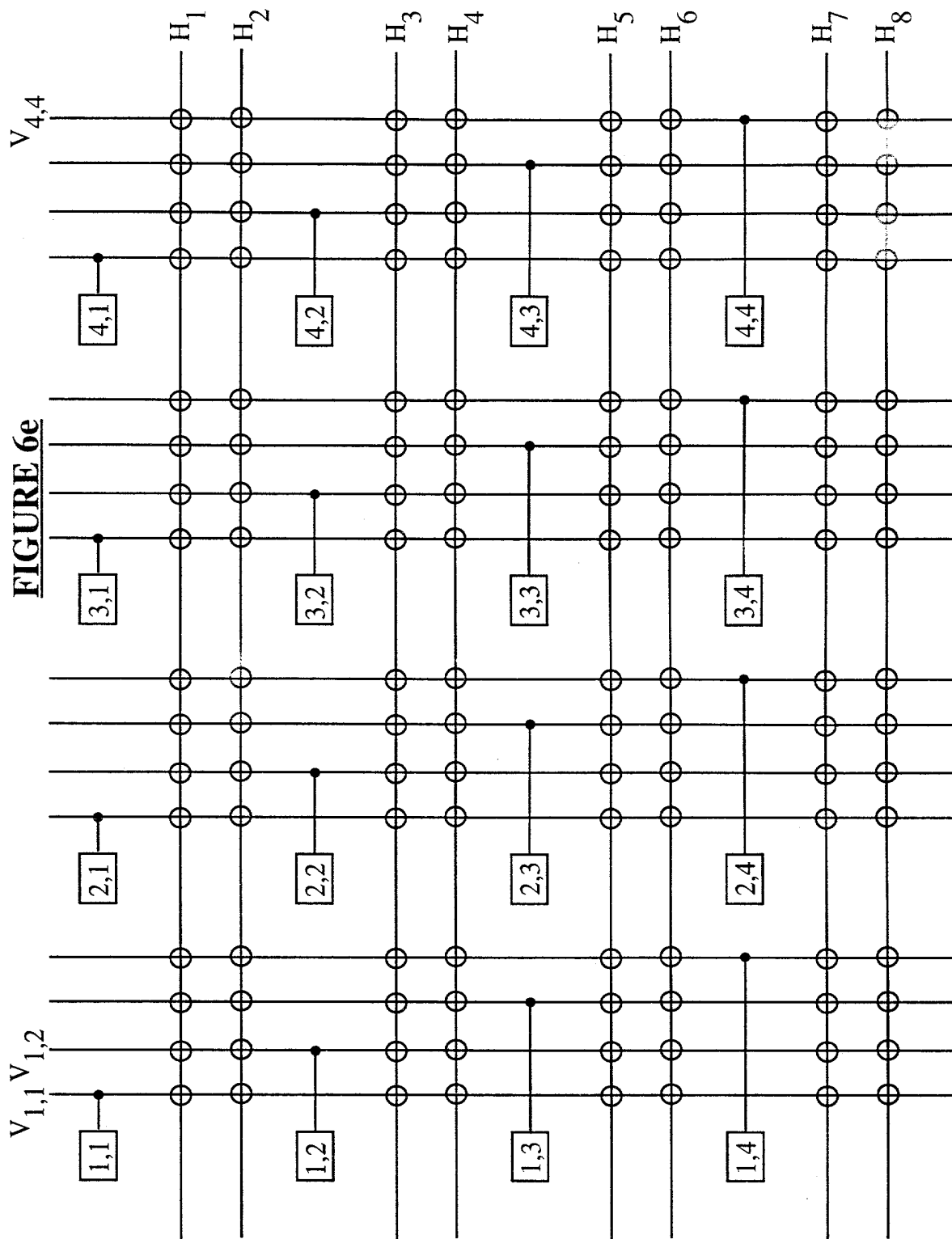


FIGURE 6c

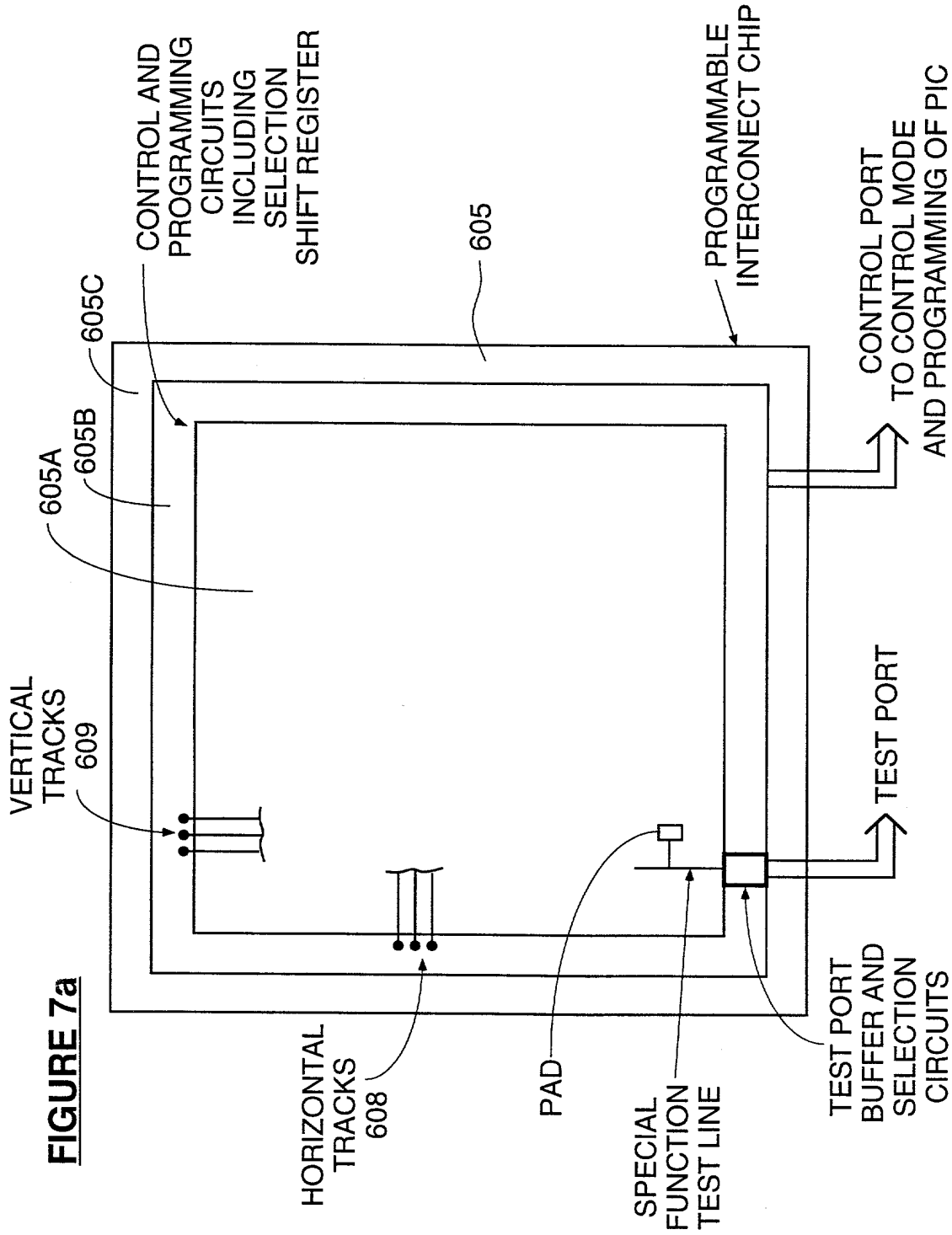
14/21



15/21



16/21



17/21

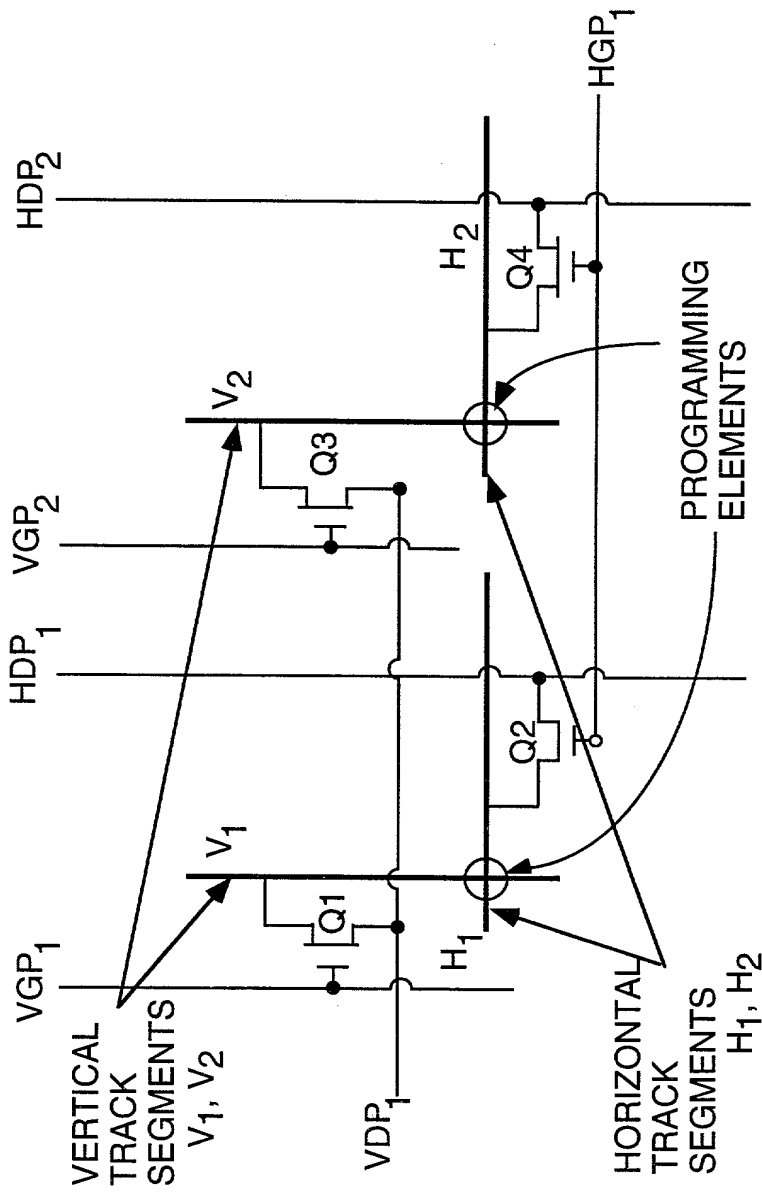


FIGURE 7b

18/21

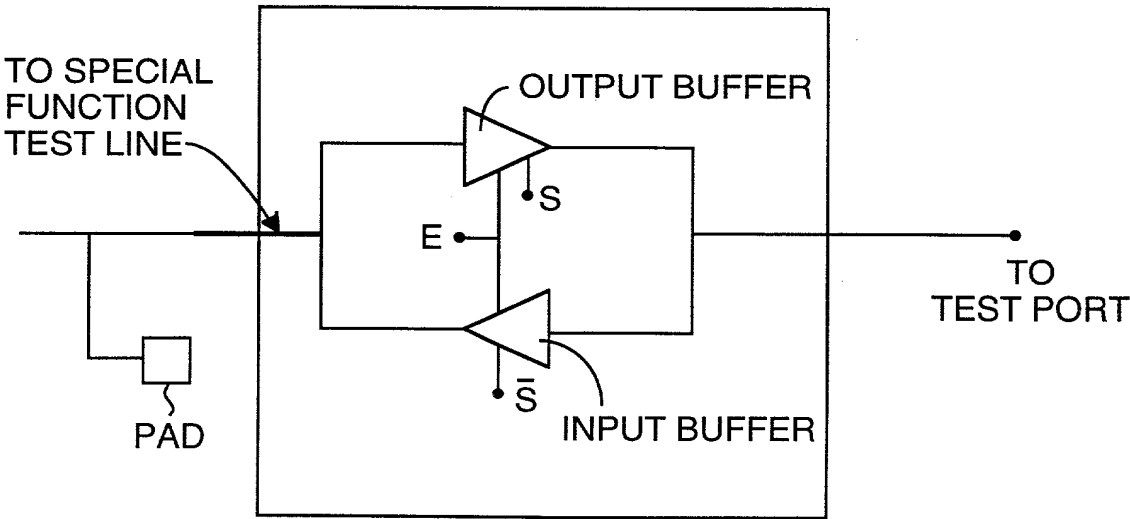
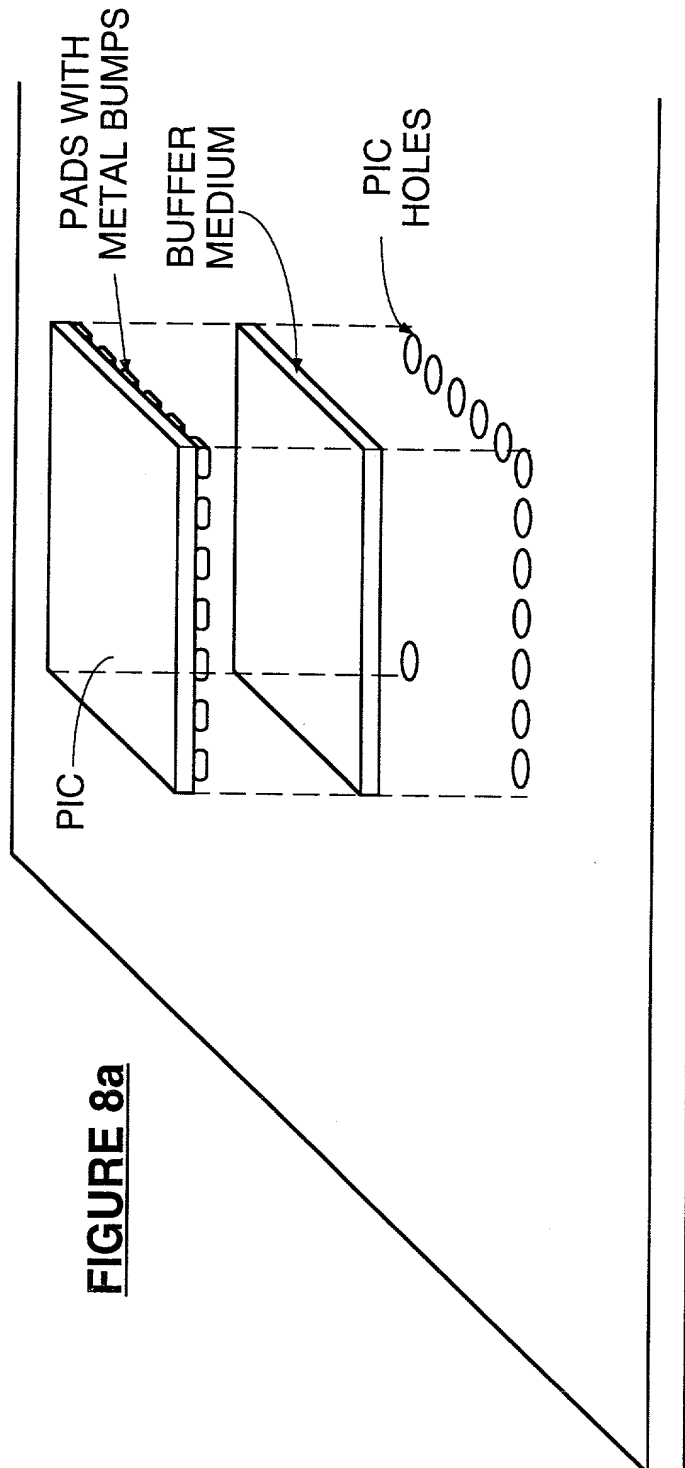


FIGURE 7c

20250727 10:33:07

19/21



20/21

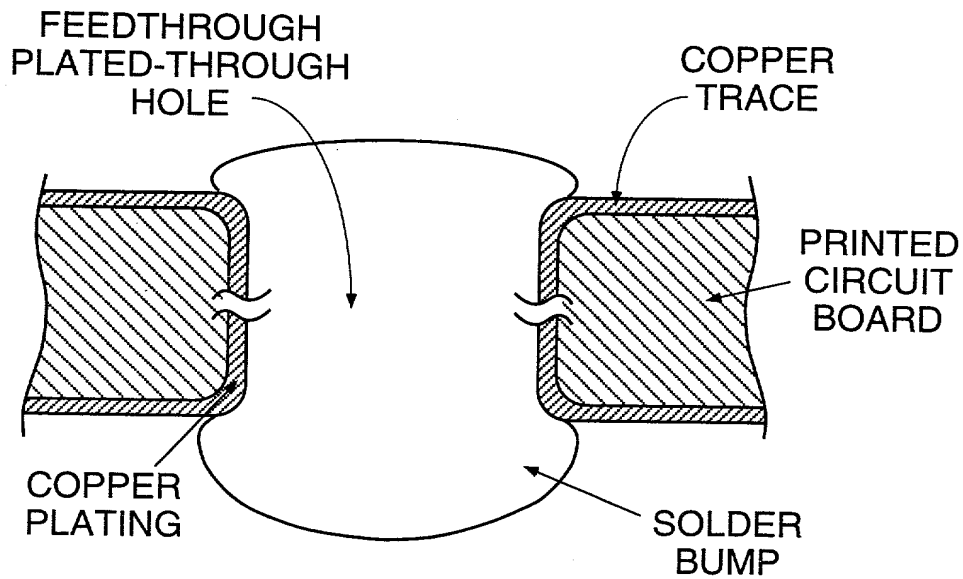


FIGURE 8b1

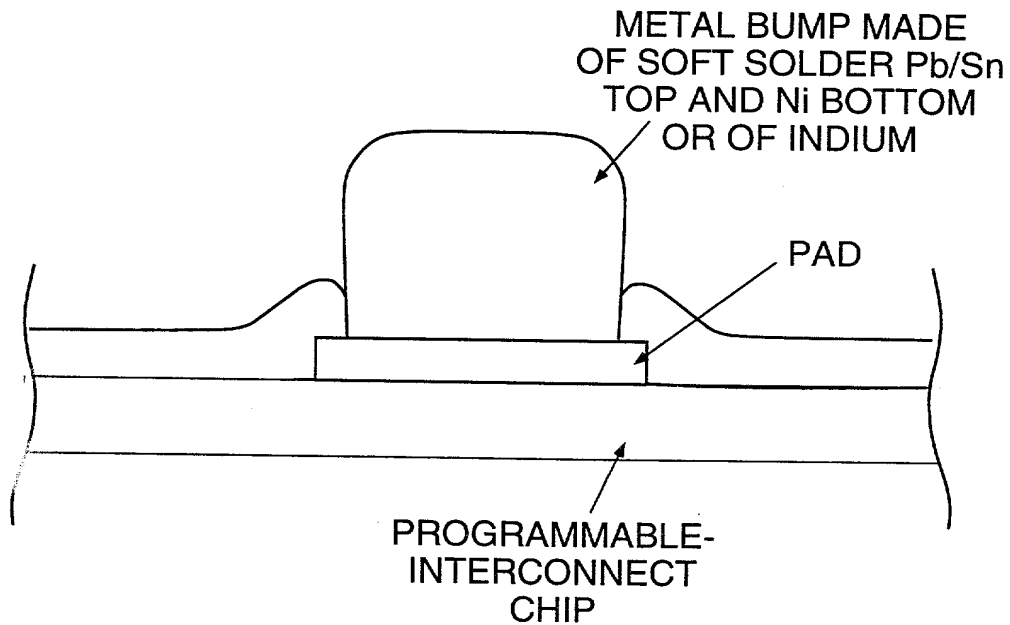


FIGURE 8b2

21/21

FIGURE 9

